Silica – Ti – Arm – OMAP Workshop

Be among the first to get an in-depth training on Industry's First Architecture based on the ARM Cortex-A8 core.

More intuitive user interface, advanced graphics and the ability to connect all devices to the Internet – Texas Instruments' OMAP processors, based on the market's first broad offering of the ARM Cortex-A8 core, provide an unprecedented combination of high performance embedded processing at industry's best performance/power ratio in a single chip.

With more than four times the processing capability of today's 300 MHz ARM9 devices, the superscalar Cortex-A8 core runs up to 600 MHz and is integrated into four new OMAP35x applications processors for a wide range of possible applications, including portable infotainment and industrial devices, points of sale, digital signage and low-power medical devices.

Seminar Content

- ARM Cortex-A8 Core
- Neon SIMD Unit
- OMAP processor overview
- Graphics/Imagination Technology
- DSP64x Capability
- Linux Kernel – Cortex-A8 Core
- Tool Chain – Compiler, linker, WinCE, Linux Tools
- Tools – Code Composer Studio
- Linux/WinCE BSP
- Power Supplies / Peripherals
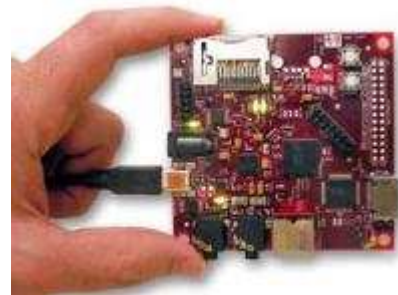
Workshop Details

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Date & Venue
31st March 2009
Registration & Coffee 8:30
Session start at 9:30 – End 16.30

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B-1860 Meise

TEXAS INSTRUMENTS

ARM

Silica OMAP Workshop 2009
Fax Back Form - +32/2/709.98.10

❑ Yes, I will attend the Silica OMAP 35xx Workshop

❑ No, I'm unavailable to attend, but I would like to have more information

Name: _____

Company: _____

Address: _____

Zip: _____   City: _____

Phone: _____

Email: _____

Other Colleagues that will attend : _____

_____