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View is through the processor/top-side, not connector/bottom-side, so mirror accordingly. This is intended to be convenient for designing a base-board.

Pin identifiers in *underscores italics* are for the MSPM0 microcontroller.

GPIO identifiers with leading 'm' are for internal M4 MCU microcontroller.

PRU options are defined by recommended io-set. Other options may or may not work.